02-11- □3 G R U B E R



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February 10, 2003

## VIA USPS EXPRESS MAIL Mailing Label #EV 229 128 595 US

Commissioner for Patents and Trademarks Washington, D.C. 20231

Re:

Patent Application for:

"ESD Improvement by a Vertical Bipolar Transistor with Low Breakdown

Voltage and High Beta"

Serial No.:

10/051,639

Attorney Docket:

TI-32536

Our File:

50000.0002

Dear Sir:

Enclosed for filing please find the following items relating to the above-identified application:

- (1) Election Document; and
- (2) Postcards.

Please file the above documents and return the date-stamped postcards to the corresponding addresses, as indicated. In the meantime, if you have any questions or comments concerning this matter, please call the undersigned. Otherwise, please accept the enclosed.

Respectfully submitted,

Gary C Honeyout

GCH:glc Enclosures

cc: W. James Brady, III (Texas Instruments Incorporated, w/ encls.)